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APPLICANTS

Yasushi Shimada, Tsukuba-shi, Ibaraki, JAPAN;
Yoshitaka Hirata, Shimodate-shi, Ibaraki, JAPAN;
Hiroyuki Kuriya, Shimodate-shi, Ibaraki, JAPAN;
Kazuhisa Otsuka, Shimodate-shi, Ibaraki, JAPAN;
Masanori Yamaguchi, Tsukuba-shi, Ibaraki, JAPAN;
Yuichi Shimayama, Shimodate-shi, Ibaraki, JAPAN;
Ken Madarame, Yuki-shi, Ibaraki, JAPAN;
Etsuo Mizushima, Yuki-shi, Ibaraki, JAPAN;
Yuusuke Kondou, Shimodate-shi, Ibaraki, JAPAN;
Kazumori Yamamoto, Tsukuba-shi, Ibaraki, JAPAN;

** CONTINUING DATA *****

This application is a 371 of PCT/JP03/06860 05/30/2003

** FOREIGN APPLICATIONS *****

JAPAN 2002-209650 07/18/2002
JAPAN 2002-209639 07/18/2002
JAPAN 2002-259291 09/04/2002
JAPAN 2002-259284 09/04/2002
JAPAN 2002-324238 11/07/2002

Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 59	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

ADDRESS

20457

TITLE

Multilayer wiring board, method for producing the same, semiconductor device and radio electronic device

FILING FEE RECEIVED 3250	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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